

T5321, T5322, T5323, T5324 T5421, T5422, T5423, T5424 5X7 mm Surface Mount High Reliability Tristate/Non-Tristate, 1 MHz to 100MHz

Hi-Reliability Product Specification

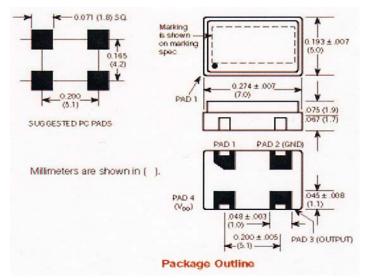
Features

- Leadless chip carrier package is hermetically sealed for superior aging and field performance
- Crystal angle controlled to +/- 1 minute for excellent temperature stability
- 168 hour Class B burn-in and extensive environmental testing for best performance in rugged field environments
- Start-up time less than 10 ms, typical
- Tristate option available
- Calculated MTBF is 3.8 X 10⁶ hours at 125°C

Typical Applications

 Surface Mounted PCB projects requiring high reliability HCMOS clock waveforms

Models	Operating	Frequency Stability
	Temperature	
T5321	-55 to + 85°C	+/0025% (+/- 25ppm)
T5322	-55 to + 85°C	+/005% (+/- 50ppm)
T5323	-55 to + 125°C	+/0075% (+/- 75ppm)
T5324	-55 to + 125°C	+/005% (+/- 50ppm)
T5421	-55 to + 85°C	+/0025% (+/- 25ppm)
T5422	-55 to + 85°C	+/005% (+/- 50ppm)
T5423	-55 to + 125°C	+/0075% (+/- 75ppm)
T5424	-55 to + 125°C	+/005% (+/- 50ppm)

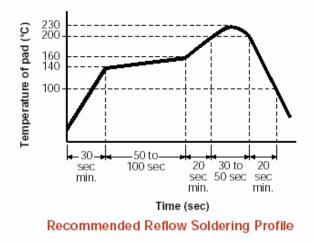


Description

These high reliability oscillators provide HCMOS clock waveforms for applications subjected to the most stringent environmental conditions. They are mechanically robust and weigh less than 0.2 grams. This 5X7 mm SMD package has a hermetic seal, thus ensuring the integrity of each oscillator. Each oscillator is burned-in at 125°C for 168 hours, temperature cycled and centrifuged then fully tested in accordance with Table 1. Reliability tests are performed per Table 2. The calculated MTBF is 3.8×10^6 at 125° C.

Connections

Pad	T5321, T5322, T5323, T5324	T5421, T5422, T5423, T5424
1.	N.C.	Tristate
2.	Ground	Ground
3.	Output	Output
	+3.3V, V _{DD}	+3.3V, V _{DD}



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ELECTRICAL SPECIFICATIONS

Frequency Range

Fixed Output Frequency Stability	1 MHz to 100MHz Includes calibration at 25°C, operating temperature, change of input voltage, change of load, shock and vibration.				
		MIN	ТҮР	MAX	
UNITS Input Voltage, V _{DD} Input Current		3.0	3.3	3.6 16	volts mA
Waveform Symmetry, Measured at 50%V _{DI}	D	40/60	45/55	60/40	percent
Rise and Fall Time CMOS, 15 pf,					
20 to 80% (<60	0 MHz)		3.0	4.0	ns
20 to 80% (<u>></u> 60 CMOS, 30 pf,			2.0	2.5	ns
20 to 80% (<60	0 MHz)		4.0	5.0	ns
20 to 80% (<u>></u> 60	0 MHz)		3.0	4.5	ns
"Zero" Level, Sinking 16 mA				0.4	volts

"One" Level		
Sourcing 8 mA	V_{DD} -0.4V	
Aging		
First year	3	
After first year	1	
Input Requirements for Pin 1.:		

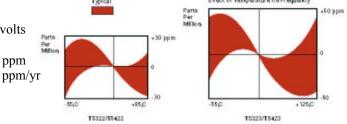
ENVIRONMENTAL SPECIFICATIONS

Shock-1000 Gs, 0.35 ms, ¹/₂ sine wave, 3 shocks in each plane Vibration-10-2000 Hz of .06" d.a. or 20Gs, whichever is less Humidity-Resistant to 85° R.H. at 85°C

Table 1

Each unit undergoes the following:

Each and analy			
 Stabilization Bake 	MIL-STD-883 Method 1008, Cond, B		
2. Temperature Cycling	MIL-STD-883 Method 1010, Cond, B		
3. Constant Acceleration	MIL-STD-883 Method 2001, Cond, A		
4. Burn-in	MIL-STD-883 Method 1015, Cond B		
	(125°C for 168 hours with bias)		
5. Fine Leak	MIL-STD-883 Method 1014, Cond. A1		
6. Gross Leak	MIL-STD-883 Method 1014, Cond C		
7 Electrical Test at 25°C	and temperature extremes, as follows:		
7. Electrical Test at 20. C	una temperatare entremes, as renews.		
A. Frequency	F. Duty Cycle		
B. Current	G. Frequency at 3.6V		
C. Rise Time	H. Frequency at 3.0V		
D. Fall Time	I. "Zero" logic level		
E. Duty Cycle	J. "One" logic level		
E. Duty Cycle	K. Tristate		
	K. Instate		
T (D (
Test Data on ea	ch unit is available for additional cost		
-			
Typical	Effect of Temperature on Frequency		
	Parts +50 ppm		
	Million		
+30 p	pm		



THERMAL CHARACTERISTICS

Thermal Resistance

From Junction to Case, RØjc 16 °C/Watt

SURFACE MOUNT APPLICATION

These packages are designed for reflow soldering in accordance with recommended profiles. For hand-soldering, the temperature of the iron should not exceed 400°C for three seconds.

volts

ppm

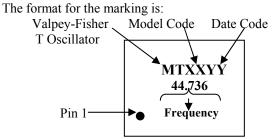


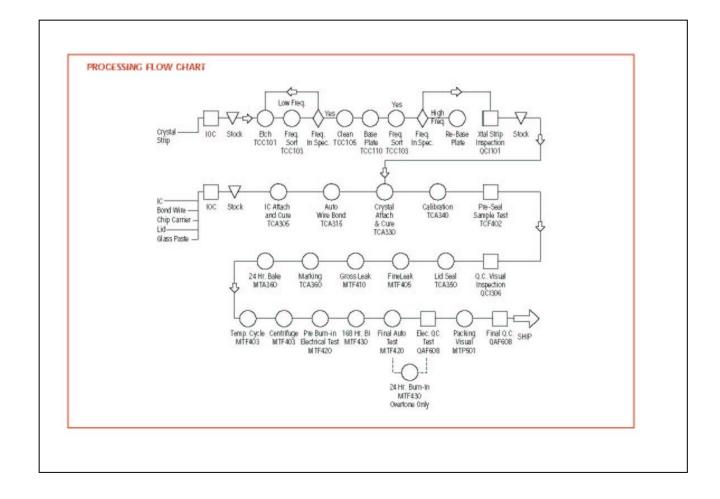
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MECHANICAL SPECIFICATIONS

Leak-MIL STD 883, Method 1014, Condition A1 and C1 Case-Hermetically sealed ceramic LCC Pads-60 microinch of gold over nickel Resistance to Solvents-MIL STD 202, Method 215 Marking-Epoxy ink or laser engraved

MARKING SPECIFICATION

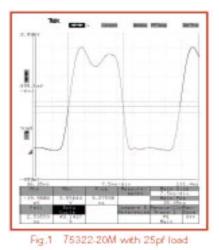




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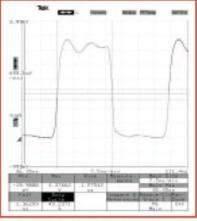
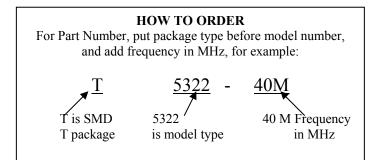


Fig. 2 T5322-20M without load



	LIABILITY TEST ONS FOR QUAI	PROCEDURE RTZ CRYSTAL OSCIL	LATORS
	haracteristics at r T5322 and T54	-55, 25 and 125°C 22)	
Frequency	@ 3.0, 3.3 and 3 ry (Duty Cycle) rrent e levels		
Package	vidth eal (Visual)	ı, discoloration, etc.) İty	
II. Group B 1000 hrs a	ging at or above	125°C, 3.3V VDC, w	ith proper load
III.Group C – A	If units have pa	assed Group A test	ing
A. Subgrou	p 1 – 8 pcs.		
Standard MIL-STD-883	Condition METHOD 2002 COND. B	Description Mechanical shock 1500 g's, 0.5ms 5 blows, 6 axis	End point measurement Frequency Output waveform
MIL-STD-883	METHOD 2007 COND: A	Vibration, var. freq. 20 g's, .06" disp., 20- 20,000-20 Hz	Frequency Output waveform
MIL-STD-883	METHOD 2003	Solderability	Visual 95% coverage
B. Subgrou	p 2 - 4 pcs (One-I	nalf of Subgroup 1)	
MIL-STD-883	METHOD 1011 COND. B	Thermal Shock Liq. to liq. -55 to 125°C, 15 cycles	Frequency Output waveform
MIL-STD-202	METHOD 105 COND. B.	Altitude, 3.44 inch Hg, 12 hrs	Frequency Output waveform
MIL-STD-883	METHOD 1004	Moisturé resist. with 3.3V applied 25-65°C, 90 to 100% RH, 10 cycles	Frequency Output waveform
MIL-STD-202	METHOD 210 COND. A.	Resistance to Solder Heat Immersion @350°C 3.5 sec	Frequency Output waveform
		half of Subgroup 1)	
Standard	Condition Storage Temp. No. Oper.	Description 24 hrs. @ –55°C 24 hrs. @ 125°C	End point measurement Frequency Output waveform
MIL-STD-883	METHOD 1009 COND. A	Salt Atmosphere 24 hrs. @ 35°C .5-3.0% Solution	Frequency Output waveform Visual
MIL-STD-883	METHOD 1014 COND. A1	Fine Leak	Qs <5 x 10 ⁻⁸
MIL-STD-883	METHOD 1014 COND: C1	Gross Leak	Visual in 125°C Detector fluid

Rev. 1.0 5/04